

TYN160WP-1600T

Rev.03 - 20 March 2025

Product data sheet

1. General description

Planar passivated Silicon Controlled Rectifier in a TO247 Plus plastic package intended for use in applications requiring very high inrush current capability and high thermal cycling performance

2. Features and benefits

- High junction operating temperature capability (T_{i(max)} = 150 °C)
- Very high current surge capability
- Planar passivated for voltage ruggedness and reliability
- High thermal cycling performance
- High voltage capability

3. Applications

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- AC power control
- Lighting and temperature control
- Uninterruptible Power Supply (UPS)
- Solid State Relay (SSR)
- Traction battery charging

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes		Values		Unit
V _{DRM}	repetitive peak off-state voltage			1600		V	
$\mathbf{I}_{\mathrm{T(RMS)}}$	RMS on-state current	half sine wave; T _{mb} ≤ 97 °C; <u>Fig. 1; Fig. 2</u> ; <u>Fig. 3</u>			250		A
I _{TSM}	non-repetitive peak on- state current	half sine wave; T _{j(init)} = 25 °C; t _p = 10 ms; <u>Fig. 4; Fig. 5</u>			1600		A
		half sine wave; $T_{j(init)}$ = 25 °C; t_p = 8.3 ms			1760		А
T _j	operating junction temperature			-40 to 150		°C	
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static ch	aracteristics						
I _{GT}	gate trigger current	$V_{D} = 12 \text{ V}; \text{ I}_{T} = 0.2 \text{ A}; \text{ T}_{j} = 25 \text{ °C};$ Fig. 7		15	-	100	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; <u>Fig. 9</u>		-	-	200	mA
V _T	on-state voltage	I _T = 160 A; T _j = 25 °C; <u>Fig. 11</u>		-	-	1.50	V
Dynamic	characteristics						
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 1072 V; (V_{DM} = 67% of V_{DRM}); exponential waveform; gate open circuit; T_j = 150 °C		1500	-	-	V/µs

5. Pinning information

Pin Symbol	Description	Simplified outline	Graphic symbol
1 K	cathode		
2 A	anode		А Д К G
3 G	gate		sym037
mb A	mounting base; connected to anode		

6. Ordering information

Table 3. Ordering information								
Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date		
TYN160WP-1600T	TO247P	TYN160WP-1600TQ	Tube	30	TO247PN	25-Jun-2024		

7. Marking

Table 4. Marking codes

Type number	Marking codes
TYN160WP-1600T	TYN160WP 1600T

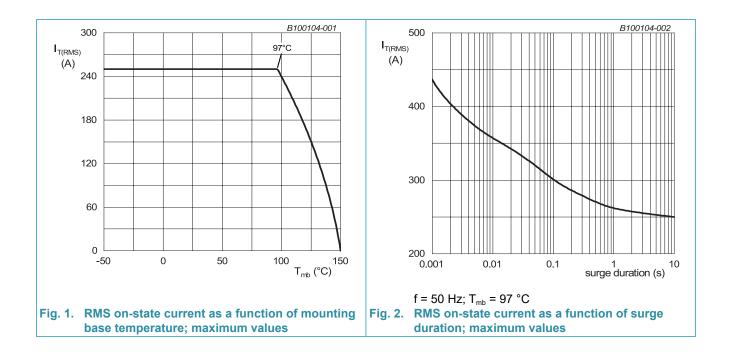
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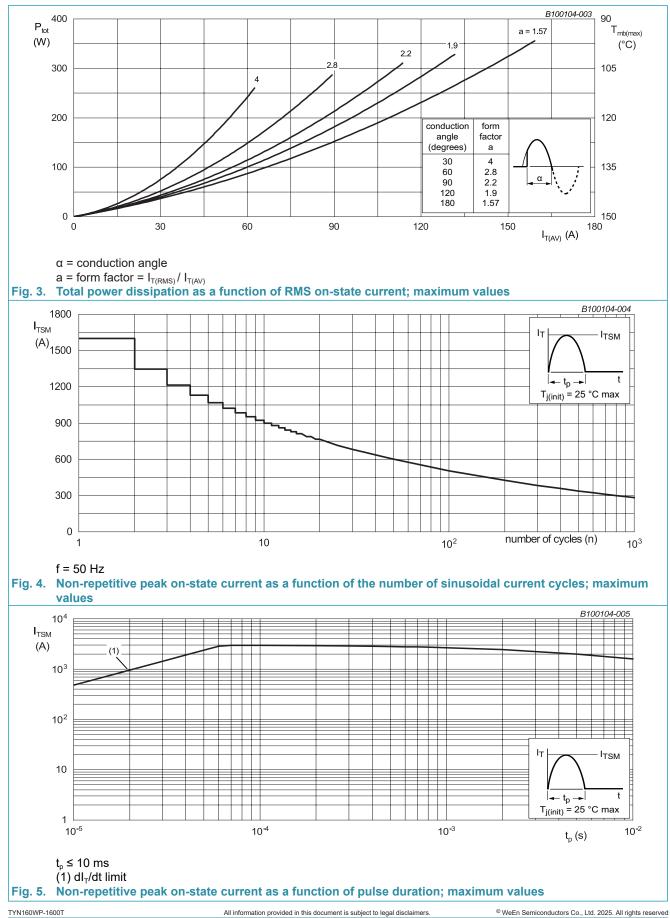
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

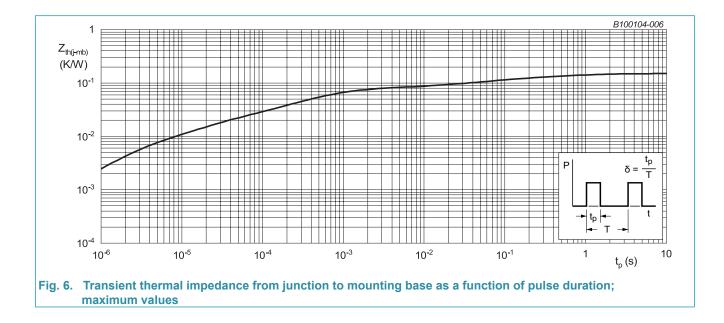
Symbol	Parameter	Conditions	Notes	Values	Unit
V_{DRM}	repetitive peak off-state voltage			1600	V
V_{RRM}	repetitive peak reverse voltage			1600	V
I _{T(AV)}	average on-state current	half sine wave; $T_{mb} \le 97 \text{ °C}$;		160	А
$I_{T(RMS)}$	RMS on-state current	half sine wave; T _{mb} ≤ 97 °C; <u>Fig. 1; Fig. 2; Fig. 3</u>		250	A
I _{TSM}	non-repetitive peak on- state current	half sine wave; T _{j(init)} = 25 °C; t _p = 10 ms; <u>Fig. 4; Fig. 5</u>		1600	A
		half sine wave; $T_{j(init)}$ = 25 °C; t_p = 8.3 ms		1760	А
l ² t	l ² t for fusing	t _p = 10 ms; sine-wave pulse		12800	A ² s
dl _T /dt	rate of rise of on-state current	I _G = 200 mA		200	A/µs
I _{GM}	peak gate current			10	А
V _{GRM}	peak reverse gate voltage			5	V
P _{GM}	peak gate power			20	W
$P_{G(AV)}$	average gate power	over any 20 ms period		0.5	W
T _{stg}	storage temperature			-40 to 150	°C
Tj	operating junction temperature			-40 to 150	°C





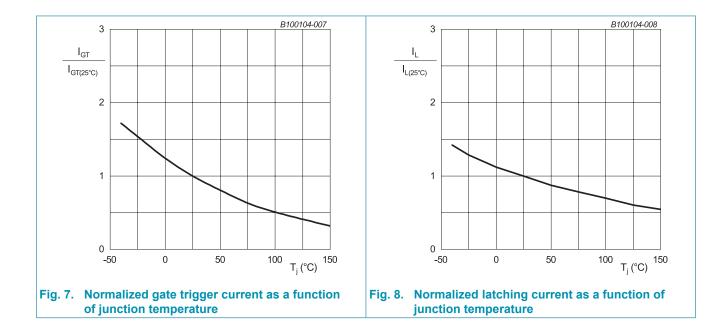
9. Thermal characteristics

Table 6. 11	able 6. Thermal characteristics							
Symbol	Parameter	Conditions	Notes	Min	Тур	Мах	Unit	
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	Fig. 6		-	-	0.15	K/W	
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air		-	45	-	K/W	

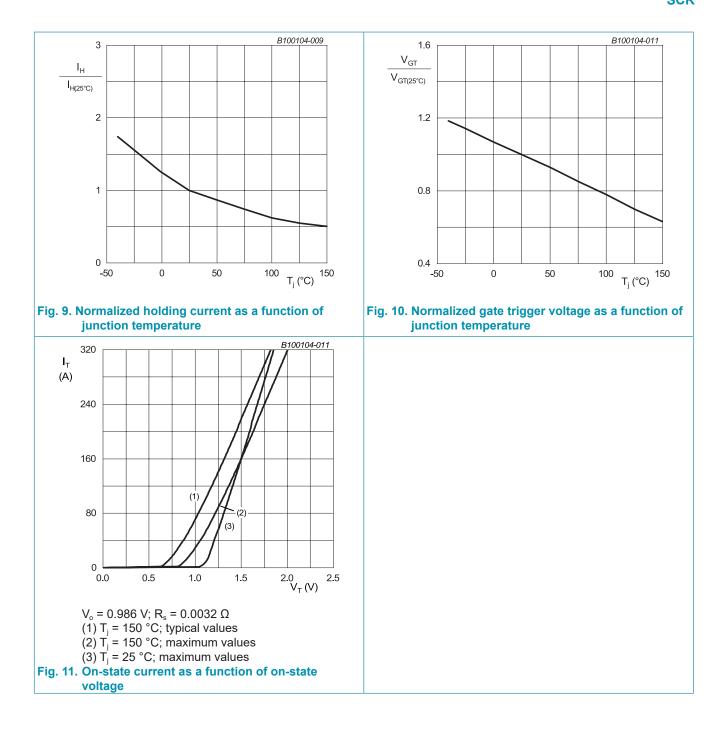


10. Characteristics

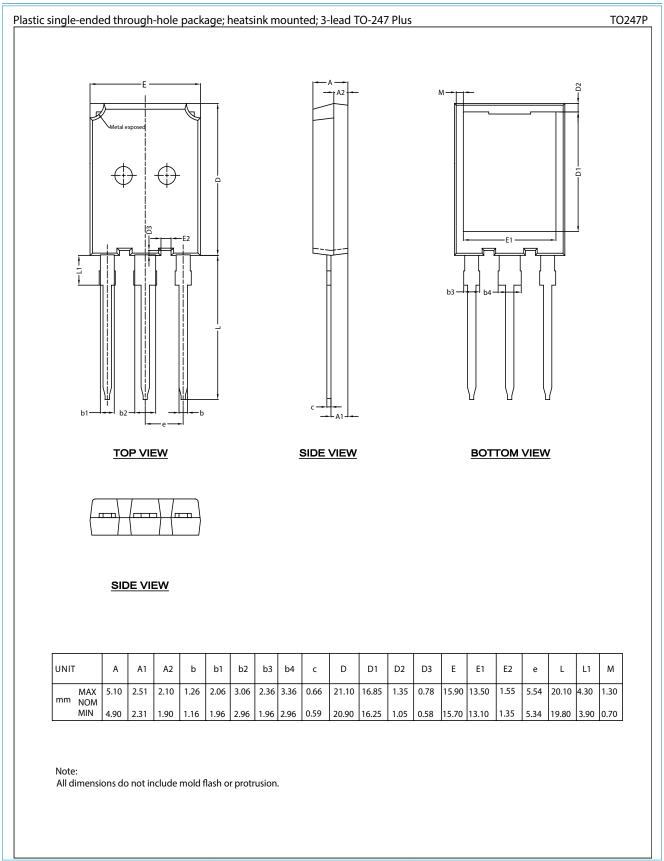
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
I _{GT}	gate trigger current	V _D = 12 V; I _T = 0.2 A; T _j = 25 °C; Fig. 7		15	-	100	mA
I _L	latching current	V _D = 12 V; I _G = 0.2 A; T _j = 25 °C; <u>Fig. 8</u>		-	-	300	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; Fig. 9		-	-	200	mA
V _T	on-state voltage	I _T = 160 A; T _j = 25 °C; <u>Fig. 11</u>		-	-	1.50	V
V _{GT}	gate trigger voltage	V _D = 12 V; I _T = 0.2 A; T _j = 25 °C; <u>Fig. 10</u>		-	0.7	1.0	V
		V _D = 1600 V; I _T = 0.2 A; T _j = 150 °C		0.25	0.45	-	V
I _D	off-state current	V _D = 1600 V; T _j = 25 °C		-	-	100	μA
		V _D = 1600 V; T _j = 150 °C		-	-	15	mA
I _R	reverse current	V _D = 1600 V; T _j = 25 °C		-	-	100	μA
		V _D = 1600 V; T _j = 150 °C		-	-	15	mA
Dynamic	characteristics					,	
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 1072 V; (V_{DM} = 67% of V_{DRM}); exponential waveform; gate open circuit; T_j = 150 °C		1500	-	-	V/µs
t _{gt}	gate-controlled turn-on time	$I_{TM} = 50 \text{ A}; V_D = 800 \text{ V}; I_G = 0.2 \text{ A};$ $dI_G/dt = 5 \text{ A}/\mu\text{s}; T_j = 25 \text{ °C}$		-	2	-	μs
t _q	commutated turn-off time			-	150	-	μs



TYN160WP-1600T



11. Package outline



12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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